

Integrated Communications Systems Modeling

WInnComm 2016

R. Muralidharan

r.muralidharan@TataPowerSED.com

Vince Kovarik, PhD

vince.kovarik@prismtech.com

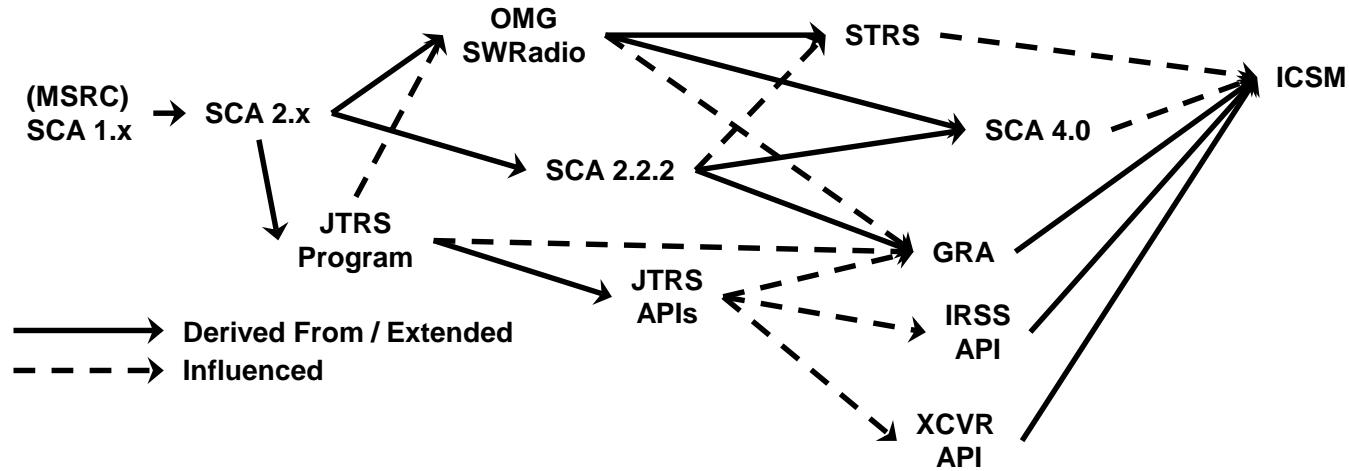
Objectives

- **Focus on the integration of hardware and software architecture specification for communications systems.**
- **Leverage prior work in SDR standards and systems to:**
 - Develop an Open Modular Radio Architecture using industry standard modeling languages and tools.
 - Promote the re/use of the architecture through model extensions and specialization.
 - Provide a reference architecture in the form of a SysML/UML model.
- **Promote the adoption and use of the Open Modular Architecture within the forum and industry.**

Rationale

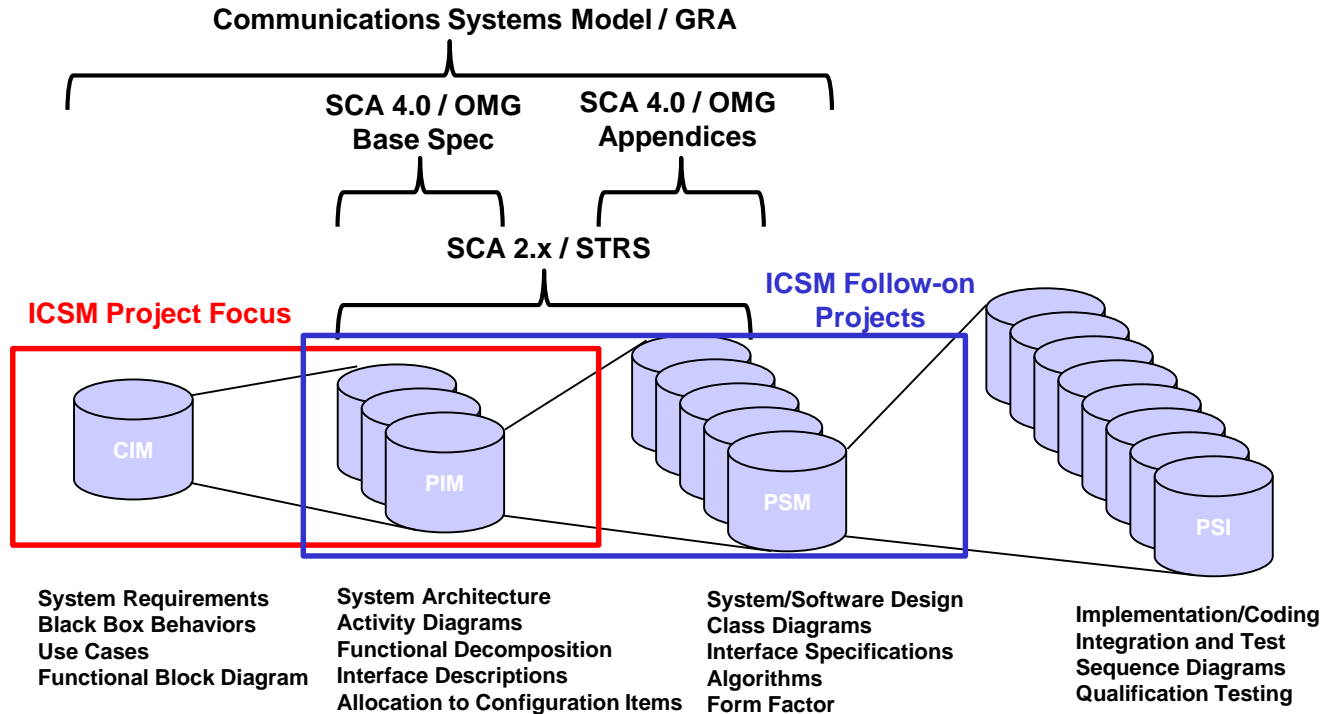
- **Standardization to date in SDR systems has primarily focused on a particular aspect of the system, e.g the software infrastructure.**
- **The hardware architecture has been traditionally performed as a semi-independent process.**
- **This approach may lead to integration and performance issues.**
- **This WInnF project was initiated to focus on developing an extensible, integrated systems model.**

Evolution of Standards and Specifications

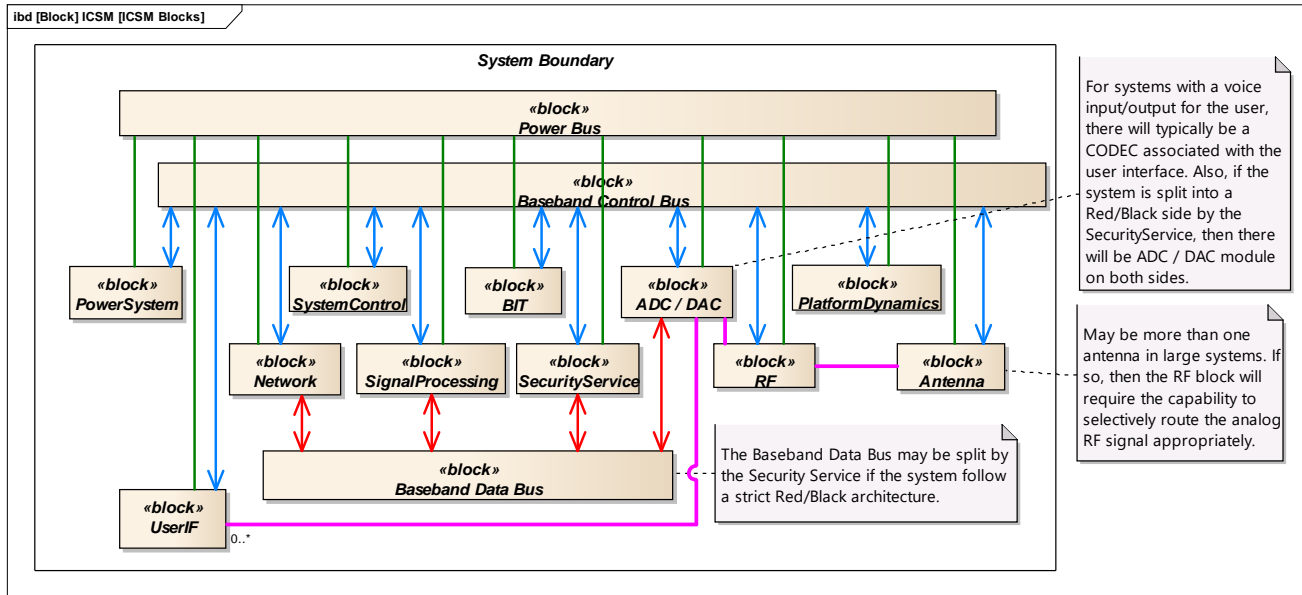


- SCA was the foundation specification of the JTRS program
- Objective of the OMG effort was to establish SCA as an industry standard
- STRS targeted for space deployed systems
- GRA originally started as alternative to SCA for A2G systems, e.g. SATCOM, with focus on Systems Model
- ICSM will leverage GRA work to provide a reference design model for communications systems

Focus Area of Standards



ICSM SysML Functional Model

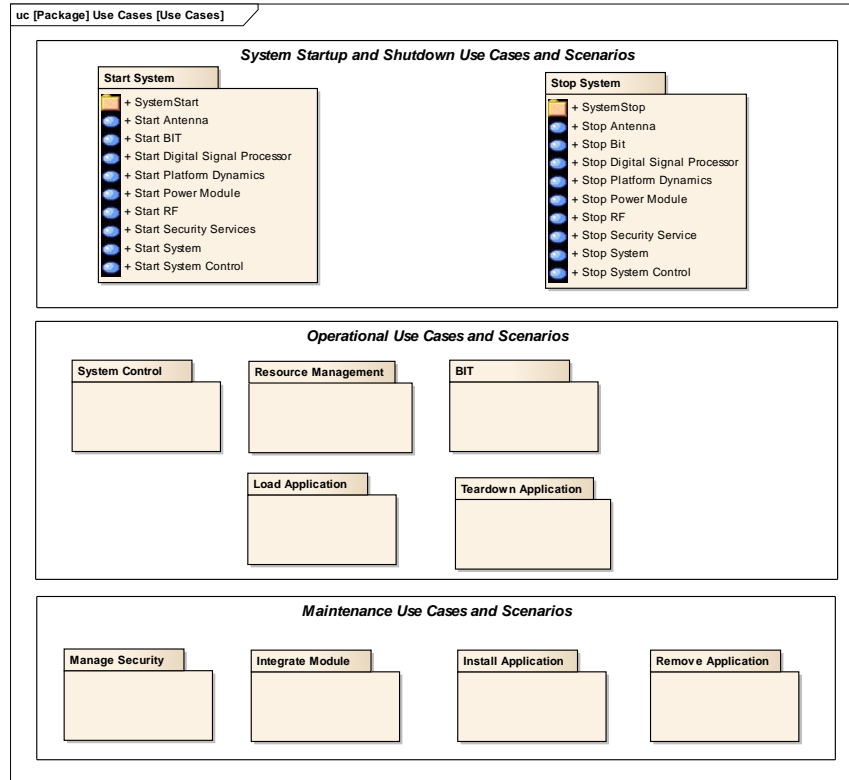


SysML enables a system engineering view

Each element has data and information attached to the element.

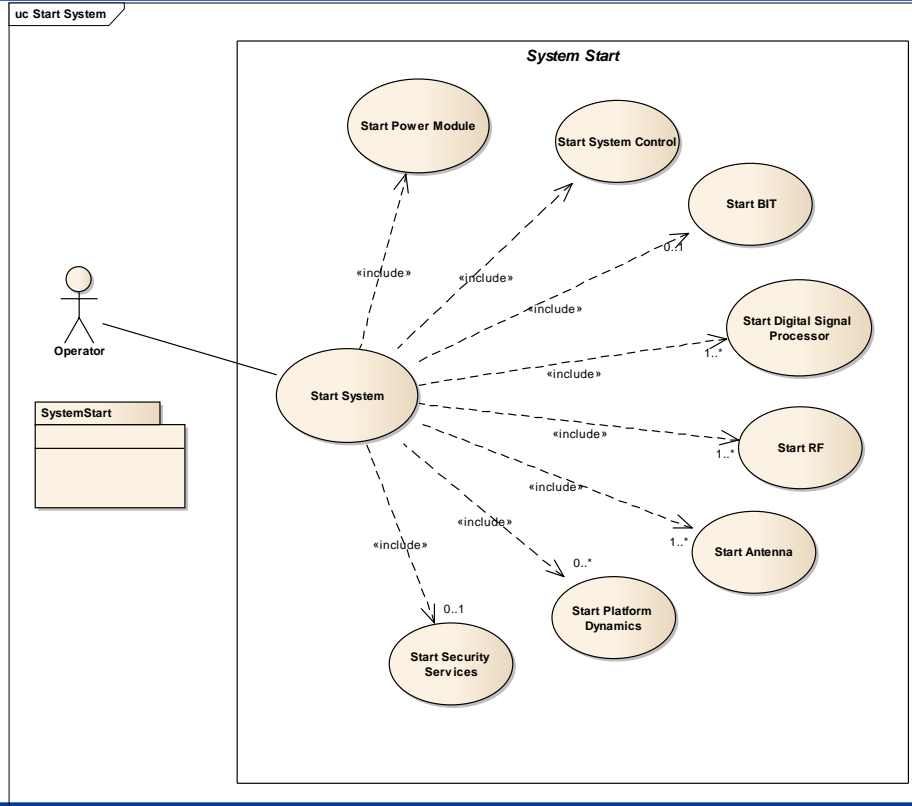
This promotes multiple views but a single model of the system.

ICSM Use Case Organization



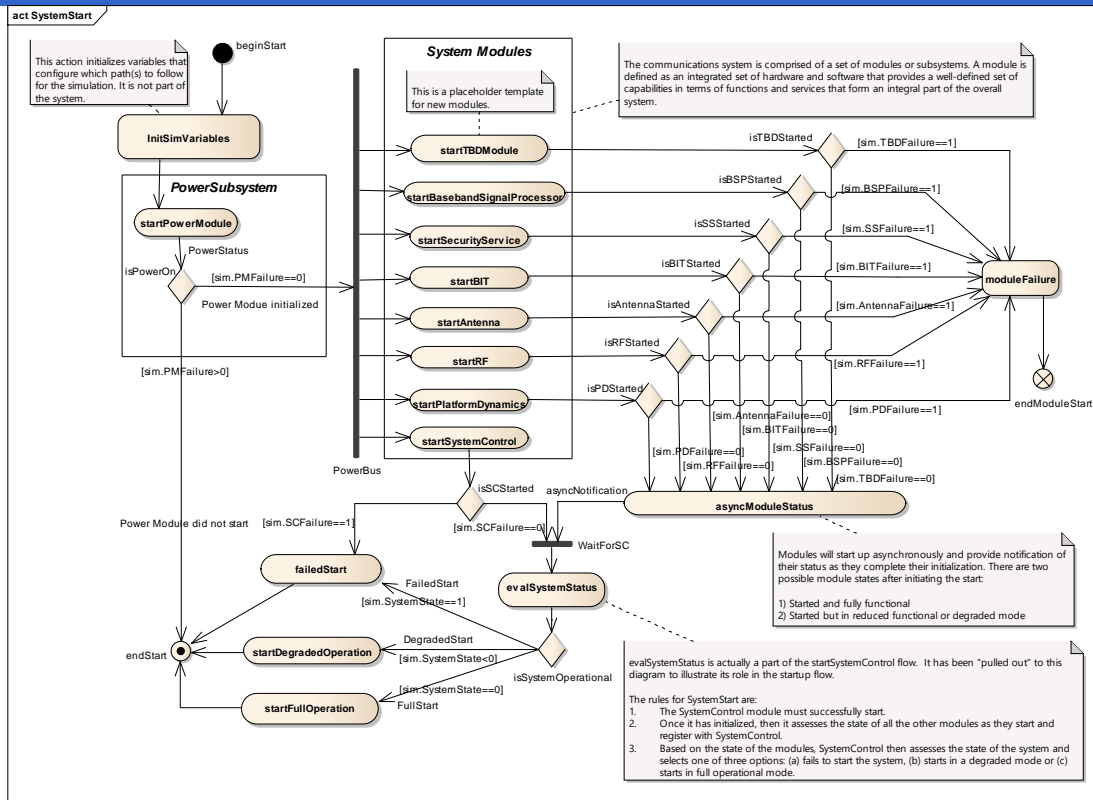
Slide 7

Start System Use Case



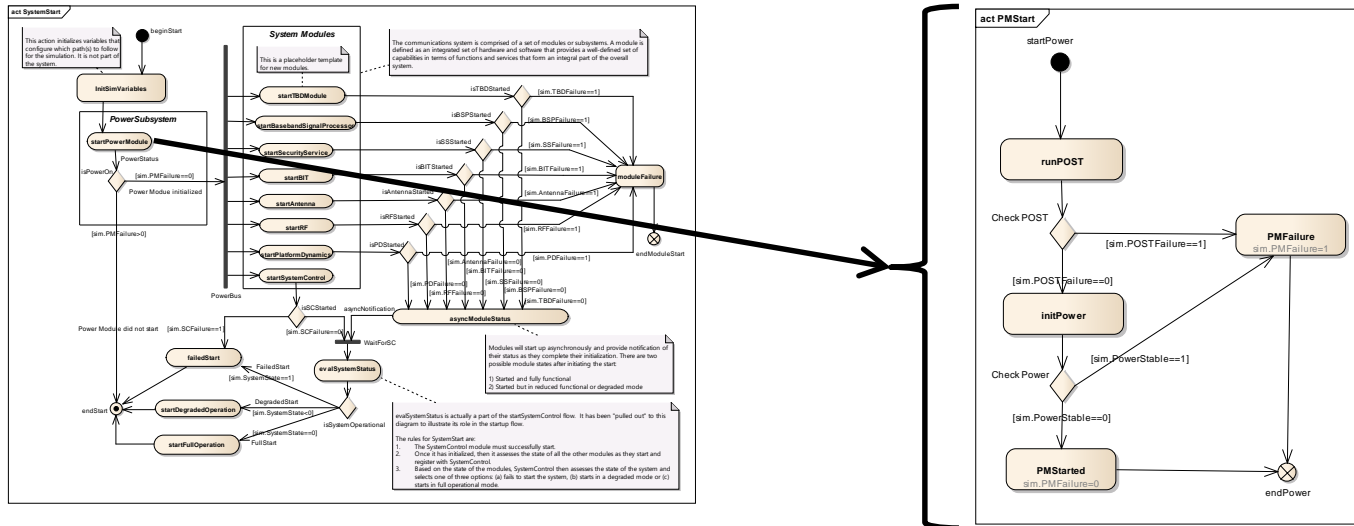
Slide 8

System Start Activity Diagram



Slide 9

Activity Decomposition



Each activity can be decomposed to lower level detail
This allows incremental development of use cases to validate operational scenarios and requirements

Component Connections and Interfaces

Low-level module interface defined through port interconnections.

Activity diagram defines control and data interfaces.

Interfaces and Hardware Abstraction

Mapping Deployment

Status

- Initial work on setting up a model database repository.
- Initial use cases for startup.
- Some reference models for current systems were considered.
- Project currently halted due to resource constraints.